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ADS 1.1: Scatterloading of mixed ARM and Thumb projects with ...
Sep 9, 2008 ... The CodeWarrior IDE offers an "**Thumb ARM** Interworking Image" project
template which you can use to create a **mixed ARM/Thumb** project. ...
infocenter.arm.com/help/topic/com.arm.doc.faqs/ka3563.html - 10k -
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RealView® Developer Kit Debugger User Guide 12.8.18. Interworking ...
You can **mix** C and C++ code for **ARM** and **Thumb**, provided that the code conforms to the
requirements of the **ARM** and **Thumb** Procedure Call Standards. ...
infocenter.arm.com/help/topic/com.arm.doc.dui0234b/CDCCEIFG.html - 14k -
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arm - mixed thumb/arm mode code: msg#00143 gcc.cross-compiling
arm - **mixed thumb/arm** mode code: msg#00143. gcc.cross-compiling ... (core
computationally intensive routines) compiled as **ARM** ...
osdir.com/ml/gcc.cross-compiling/2002-11/msg00143.html - 13k - Cached - Similar pages

Mixed Thumb & Arm Code: msg#00019
Specifically - I'm looking for a doc that might have this title: "How to write **ARM** and **THUMB**
assembler functions that interface to GCC compiled C and C++ ...
osdir.com/ml/ports.arm.general/2002-12/msg00019.html - 12k - Cached - Similar pages
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https://cia.vc/stats/author/drow 1615 2 2 CIA Perl client for GCC ...
(LDEMUL_AFTER_OPEN): Delete. ld/testsuite/ * ld-**arm**/farcall-**mix**.d, ... (
arm_build_one_stub): Encode **Arm** and **Thumb** instructions separately to take ...
https://cia.vc/stats/author/drow/.xml - 23k - Cached - Similar pages

Joel Brenner - Does GDB suports mixed ARM_code (ARM+Thumb) debugging?]
Jan 30, 2001 ... To: Nicolas dot Thery at Symbian dot com; Subject: Re: Does GDB suports
mixed ARM_code (**ARM+Thumb**) debugging? From: Joel Brenner < joel dot ...
sources.redhat.com/ml/crossgcc/2001-01/msg00247.html - Similar pages

[eLua-dev] Mixed arm and thumb code
Previous message: [eLua-dev] **Mixed arm** and **thumb** code; Next message: [eLua-dev]
Mixed arm and **thumb** code; Messages sorted by: [date] [thread] [subject ...
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[eLua-dev] Mixed arm and thumb code
[eLua-dev] **Mixed arm** and **thumb** code. Mike Panetta panetta.mike at gmail.com. Sat Mar
28 21:30:51 CET 2009. Previous message: [eLua-dev] **Mixed arm** and **thumb** ...
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[PDF] Profile Guided Selection of ARM and Thumb Instructions
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in some cases the **mixed** code outperforms both **ARM** and **Thumb** .... generation of **mixed**
ARM and **Thumb** code along with their eval- uation. ...
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ABSTRACT Profile Guided Selection of ARM and Thumb Instructions ...
We propose profile guided algorithms for generating **mixed ARM** and **Thumb** code for
application programs so that the resulting code gives significant code size ...
citeseer.ist.psu.edu/krishnaswamy02profile.html - Similar pages

by A Krishnaswamy - All 3 versions

Re: Mixed linking of ARM/THUMB object files

Jan 13, 2000 ... Re: **Mixed** linking of **ARM/THUMB** object files. To: "crossgcc mailinglist" <
crossgcc at sourceware dot cygnus dot com>, "binutils mailinglist" **...**
www.sourceware.org/ml/binutils/2000-01/msg00066.html - 7k - Cached - Similar pages

arvind - ARM/Thumb mixed code

Oct 17, 2001 ... Greetings, Is there some way by which I can generate **mixed ARM/Thumb**
code i.e certain portions of the code be 16-bit while others are 32-bit **...**
gcc.gnu.org/ml/gcc-help/2001-10/msg00157.html - Similar pages

[Buildroot] [PATCH] Add thumb support to buildroot for the ARM ...

Mar 12, 2008 ... Interworking allows **Thumb** code to call **ARM** code and vice-versa, allowing
a **mixed Thumb/ARM** userspace. Enabling interworking at the toolchain **...**
lists.busybox.net/pipermail/buildroot/2008-March/019622.html - 10k -
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FROM EDN EUROPE: ARM restructures limbs of instruction set - 7/10 ...

Thumb-2 is now a **mixed** 16- and 32-bit instruction set with **Thumb** forms of complex **ARM**
instructions. It also offers a few completely new 16-bit instructions **...**
www.edn.com/article/CA309158.html - Similar pages

uClinux Developers Archive: RE: [uClinux-dev] uCLinux ARM thumb-elf

Also, I'm sure it would be fun to link **mixed ARM/THUMB** code... never tried it, but I love a
challenge. > -----Original Message----- > From: **...**
www.uclinux.org/pub/uClinux/archive/0054.html - 9k - Cached - Similar pages

ArmEabiPort - Debian Wiki

The EABI includes **thumb** interworking, which means that 16-bit **Thumb** and normal 32-bit
ARM instructions can be **mixed** at function-level granularity. **...**
wiki.debian.org/**Arm**EabiPort - 55k - Cached - Similar pages

[PDF] 16/32-Bit ARM-Thumb Architecture and AX Extensions

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Mixed ARM-**Thumb** Code. (Example) .code 16. ;. **Thumb** instructions folow ... .align 2. ;
make bx word aligned bx r15. ; switch to **ARM**, r15[0] not set **...**
www.ittc.ku.edu/~kulkarni/research/**thumb**_ax.pdf - Similar pages

tech-toolchain: mixing thumb and arm?

Jul 7, 2006 ... Some C files are intended to be compiled for **thumb** and some for **arm**, **....**
link h.o and m.o as a **mixed thumb** and **arm** executable hm: h.o m.o X **...**
mail-index.netbsd.org/tech-toolchain/2006/07/07/0005.html - 8k - Cached - Similar pages

1996: An ARM and a Thumb | Actuality Group

Mike produced the first **ARM/Thumb** 32/16 bit compiler to provide for **mixed** mode source
files, allowing each function within a file to be compiled with **...**
actualitygroup.com/mikes_history_contributions/1996_an_**arm**_and_a_**thumb**/ - 13k -
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'RE: openssl-0.9.8-stable-SNAP-20050805 on WinCE5.0' - MARC

TARGETCPU=ARMV4I allows 32 bit **ARM** and 16 bit **thumb** instructions to be **mixed** at a
function level. This TARGETCPU type seems to be meant for **ARM** CPU's that **...**
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